

## ASIC Designer ASIC Project Leader

The successful applicant will work in our ASIC design groups:

- **high-volt** developing Smart Power integrated circuits in industrial and automotive applications; and/or
- **low-power** developing high performance low consumption, low noise integrated circuits for sensing and medical applications

The work includes the analysis of project feasibility, system level design and verification, specification definition and all aspects of the design process.

Effective communication and coordination both internally and externally, and supervision of the design work are key elements of this role.

To fill this position, we are seeking an ambitious electronic engineer with an engineering degree (masters), experience in analogue microelectronic design and in possession of strong analytical skills. Sound knowledge of semiconductor physics and IC manufacturing processes is advantageous. The applicant must also have either good German or good French.

For further information or applications, please contact:

[mailbox@hmt.ch](mailto:mailbox@hmt.ch)

HMT microelectronic AG

Alfred-Aebi Strasse 75

CH-2503 Biel/Bienne

Tel. +41 (0) 32 365 11 81

[www.hmt.ch](http://www.hmt.ch)

### **About HMT:**

*Active in the ASIC and hybrid design industry, HMT is the eldest, and one of the largest, independent contract ASIC design houses in Europe. Since the foundation in 1978 as a spin-off of the Swiss watch industry, over 500 ASIC developments have entered into series production.*

*In today's automated world, sensors and actuators developed by HMT are present in every market segment: from industrial to automotive and medical and aeronautic.*